|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **REF, ACTUAL CURRENT & VOLTAGE DPM PCB (PA79-557/B) TEST REPORT (017.01.008.0374 ,017.01.008.0376 and 017.01.008.0377)** | | | | | | | | | | | | | | | | |
| **SR. NO.** | **Test point-->** | **TP1 wrt TP2** | **All seven segment displays, are working ok** | **TP4**  **(Actual**  **voltage calibration)**  **wrt TP2** | **TP5**  **(Actual**  **Current**  **Calibration)**  **wrt TP2** | **Check actual current on display @ different input voltages.** | | | | | **Check actual current on display.**  **@ 4V input.**  **Continuous for one minute.** | **Link**  **All Open – 500A.**  **LK2, LK3 short – 300A** | **Software rev** | **Software Revision Sticker, Potting done & Silastic applied at trim pot.** | **REMARK (IF ANY REWORK IS DONE** |
| **@Max 50V**  **Calibrated by P3 POT** | **@Max.**  **4V input**  **(Calibrate by P1 POT)** | **@ 0**  **V** | **@ 1 V** | **@ 2 V** | **@ 3 V** | **@ 4**  **V** |
| Condition--> | +5Vdc  (+/-0.2V) | OK/NOT OK | 1.50 to 1.7 V (±0.2V) | 2.9 to 3.1 V (±0.2V**)** | 0.0-0.05 v | 0.6-0.8 v | 1.4 -1.6 v | 2.1 -2.3 v | 2.9 - 3.1 V | OK/  NOT OK | OK/  NOT OK | Mention rev | OK/  NOT OK |  |
| Message on Display for  **017.01.008.0374** |  |  |  | **500 on display window >** | 10 or ref curr | 125  (±5) | 250  (±5) | 375  (±5) | 500  (±5) | Constant  500((±3)) |  |  |  |  |
| Message on Display for  **017.01.008.0376** |  |  |  | **500 on display window >** | 10 or ref curr | 125  (±5) | 250  (±5) | 375  (±5) | 500  (±5) | Constant  500((±3)) |  |  |  |  |
| For (300A) **017.01.008.0377** |  |  |  | **2.9 to 3.1 V (±0.2V)** | **0- 0.2 V** | **1.0-1.2 V** | **2.0-2.2V** | **3.0-3.2V** | **-** |  |  |  |  |  |
| Message on Display for  **017.01.008.0377** |  |  |  | **300 on display window >** | 0 or ref curr | 138  (±5) | 281  (±5) | 300  (±5) |  | Constant  300((±3)) |  |  |  |  |
| **PCB SR.NO.** | **Measured / Actual voltage** | | | | | | | | | | | | | | |
| 1 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 2 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 3 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 4 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 5 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 6 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 7 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |